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design cycle in which the functional design of an electronic circuit is converted into the representation which captures logic operations, arithmetic operations, control flow, etc. A common output of this step is RTL description. Logic design is commonly followed by the circuit design step.

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Preface ix To Students About To
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of Digital Design ix How to Use This

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Book	xii
RTL-Focused Approach	xii
Traditional Approach with Some Reordering	xii
Traditional Approach	xiii
Acknowledgements	xiii
About the Cover	xiv
About the Author	xiv
Reviewers and Evaluators	xv
CHAPTER 1 Introduction	1
1.1 Digital Systems in the World Around Us	1
1.2	

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Page 36/39

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